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	JP Japan		Inventor(s): TANAKA KOICHIRO SHIOMI TOMONORI	MATSUSHITA ELECTRIC IND CO LTD News, Profiles, Stocks and More about this company	Nov. 25, 1991 / March 14, 1990	r: JP199000063162	40 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
() \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Country:	Kind:	Inventor(s):	Applicant(s):	Issued/Filed Dates:	Application Number:	

(54) QUADRATURE AMPLITUDE MODULATOR

(57) Abstract:

PURPOSE: To dispense with adjustment in a phase shifter in which a high frequency is handled by correcting a baseband input signal used as input to a quadrature amplitude modulator at a linearly coupled circuit to correct the phase error of the phase shifter in the quadrature amplitude modulator.

COPYRIGHT: (C)1991, JPO& Japio appropriately at the linearly coupled 102, 103 in the modulator. A carrier almost 90° by the phase shifter 104, adder 105, then, modulation output 06, and are inputted to multipliers 102, 103, and are synthesized at an baseband signals at the multipliers circuit 101, and are inputted to the CONSTITUTION: Two baseband is changed to apair of orthogonal quadrature amplitude modulator carriers with phase difference of and they are multiplied by the signals are linearly coupled is issued.